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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Richard E. Fackenthal	§	Art Unit:	2133	
Serial No.:	10/687,124	§	Examiner:	Guy J. Lamarre
Filed:	October 16, 2003	§	Docket:	ITL.1046US P17448
For:	Error Correction for Multi- Level Cell Memory with Overwrite Capability	§	Assignee:	Intel Corporation

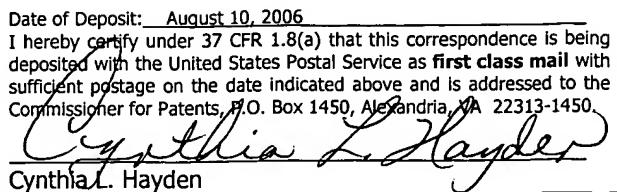
Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed June 8, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: August 10, 2006
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden